Attorney Docket No.: 42390P5768D

CLEAN VERSION

ABSTRACT OF THE DISCLOSURE

A transistor using mechanical stress to alter carrier mobility. Voids are formed in one or more of the source, drain, channel or gate regions to introduce tensile or compressive stress to improve short channel effects.

BSTZ Docket No.: 42390P5768D

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

ABSTRACT OF THE DISCLOSURE

[ABSTRACT OF THE INVENTION]

[A method of improving short channel effects in a transistor. First, a substance is

implanted in a substrate. The substrate is then annealed such that the implanted substance

forms at least one void in the substrate. Then, a transistor having a source, a drain, and a

channel region is formed on the substrate, wherein the at least one void is in the channel

region of the transistor.]

A transistor using mechanical stress to alter carrier mobility. Voids are formed in

one or more of the source, drain, channel or gate regions to introduce tensile or

compressive stress to improve short channel effects.

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		Application Number	10/045,376	三	
TRANSMITTAL		Filing Date	11-9-01	5	
FORM		First Named Inventor	Doyle, et al. 2818		
(to be used for all correspondence after initial filing)		Group Art Unit	2818		
		Examiner Name	Le, Dung Anh		
Total Number of Pages in This Submission		Attorney Docket Number	42390.P5768D		
ENCLOSURES (check all that apply)					
Fee Transmittal Form		nent Papers Application)	After Allowance Communication to Group		
Fee Attached	Drawing(s)		Appeal Communication to Board of Appeals and Interferences		
Amendment / Reply	Licensing-related Papers		Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)		
After Final	Petition		Proprietary Information		
Affidavits/declaration(s)	Petition to Convert to a Provisional Application		<u> </u>		
Extension of Time Request	Power	of Attorney, Revocation of Correspondence	Status Letter Other Enclosure(s) (please		
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or William W. Kidd, Reg. No. 31,772 Individual name					
Signature William W. Kield Date 12-23-02					
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STATES PATENT AND TRADEMARK OFFICE

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In Re Application of:)
Doyle, et al.)
Application No.: 10/045,376) Art Unit: 2818
Filed: November 9, 2001	Examiner: Le, Dung Anh
For: MOS Transistor Using Mechanical Stress To Control Short Channel Effects	Examiner: Le, Dung Anh PET CH
Commissioner for Patents Washington, D.C. 20231	DED CENTER
SURSTITUTE ARSTRACT OF	THE DISCLOSURE 1

Dear Sir:

In response to the objection raised in the Office Action mailed on September 24, 2002, Applicants hereby submit a substitute Abstract of the Disclosure.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

/2-23-02 Date:

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